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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/847,420	05/03/2001	Shuji Yamada	35.C15513	5480	
5514	5514 7590 03/17/2004			EXAMINER	
	CK CELLA HARPER	PERRY, ANTHONY T			
	30 ROCKEFELLER PLAZA NEW YORK, NY 10112		ART UNIT	PAPER NUMBER	
·			2879		
				DATE MAILED: 03/17/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)
	09/847,420	YAMADA ET AL.
Office Action Summary	Examiner	Art Unit
	Anthony T Perry	2879
The MAILING DATE of this communication ap Period for Reply	ppears on the cover sheet with	he correspondence address
A SHORTENED STATUTORY PERIOD FOR REPI THE MAILING DATE OF THIS COMMUNICATION - Extensions of time may be available under the provisions of 37 CFR 1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a re If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statu Any reply received by the Office later than three months after the maili earned patent term adjustment. See 37 CFR 1.704(b).	.136(a). In no event, however, may a reply ply within the statutory minimum of thirty (3 d will apply and will expire SIX (6) MONTH te, cause the application to become ABAN	be timely filed O) days will be considered timely. G from the mailing date of this communication. DONED (35 U.S.C. § 133).
Status		
1) Responsive to communication(s) filed on 27.	January 2004.	
·— · · · · · · · · · · · · · · · · · ·	is action is non-final.	
3) Since this application is in condition for allow closed in accordance with the practice under	ance except for formal matters	
Disposition of Claims		
4) Claim(s) 1-31 is/are pending in the applicatio 4a) Of the above claim(s) is/are withdress 5) Claim(s) is/are allowed. 6) Claim(s) 1-31 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/	awn from consideration.	
9)☐ The specification is objected to by the Examir	ner.	
10)⊠ The drawing(s) filed on <u>03 May 2001</u> is/are: a	a)⊠ accepted or b)⊡ objecte	d to by the Examiner.
Applicant may not request that any objection to th		
Replacement drawing sheet(s) including the corre		
Priority under 35 U.S.C. § 119		
a) All b) Some * c) None of: 1. Certified copies of the priority document of: 2. Certified copies of the priority document of: 3. Copies of the certified copies of the priority document of the priority document of the certified copies of th	nts have been received. nts have been received in App ority documents have been re au (PCT Rule 17.2(a)).	lication No ceived in this National Stage
Attachment(s)	n □	(DTO 442)
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 Paper No(s)/Mail Date 		mary (PTO-413) fail Date mal Patent Application (PTO-152)

DETAILED ACTION

Response to Amendment

The Amendment, filed on 1/27/04, has been entered and acknowledged by the Examiner.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1, 4-5, 9, 12-13, 17-25, and 27-30 are rejected under 35 U.S.C. 102(b) as being anticipated by Yoshioka et al. (US 5,872,541).

Regarding claims 1, 9 and 17, Yoshioka discloses an electron source forming substrate in Fig. 11(5) where an electron-emitting device is arranged, comprising a substrate 4 with an insulating material film 11 formed thereon at which surface an electron emitting device is disposed (see Fig. 8). Yoshioka teaches that the insulating layer can be an SiO₂ film (col. 11, lines50-51). Wherein the insulating material film 11 contains a plurality of metallic oxide particles 9 (see col. 8, line 44 – col. 9, line 47). Yoshioka teaches a range for the particle size that includes values that are encompassed by the limitation of 15 nm to 30 nm (col. 14, lines 46-47).

The recitation, "wherein said substrate structure is a precursor to an electron source, and said insulating material film has a surface on which an electron-emitting device of the electron source is to be arranged" has not been given patentable weight because it is considered an intended used recitation. It has been held that a recitation with respect to the manner in which a

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claimed apparatus is intended to be employed does not differentiate the claimed apparatus from a prior art apparatus satisfying the claimed structural limitations. Ex parte Masham, 2 USPQ 2d 1647 (1987).

Regarding claims 4-5 and 12-13, Yoshioka teaches a range for the thickness of the insulating/SiO₂ film which includes values that are encompassed by the limitation of 300 nm to 400 nm (col. 11, lines 52-55).

Furthermore, Yoshioka teaches a range of thickness for the insulating/SiO₂ film that is necessary for the fine metallic oxide particles to be dispersed and fixed (col. 11, lines 52-55). It has been held that where the general conditions of a claim are disclosed in the prior art, discovering the optimum or workable ranges involves only routine skill in the art. It would have been obvious to one having ordinary skill in the art at the time the invention was made to provide a workable range for the thickness of the insulating/SiO₂ film, since optimization of workable ranges is considered within the skill of the art.

Regarding claims 18-20, Yoshioka teaches the metallic oxide particles being SnO2 particles which are electron conduction oxide particles (col. 14, lines 46-47).

Regarding claim 21, Yoshioka discloses that the substrate is made of glass which inherently contains Na (col. 5, line 62).

Regarding claim 22, Yoshioka discloses that the substrate is an electron source forming substrate on which an electron source is formed (see Fig. 36).

Regarding claim 23, Yoshioka discloses a conductive film 3a containing an electronemitting portion 10 (col. 31, lines 39-52).

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Regarding claim 24, Yoshioka discloses a plurality of electron-emitting devices arranged in a matrix wiring composed of a plurality of row directional wirings and a plurality of column directional wirings (see Fig. 39a-d).

Regarding claim 25, Fig. 36 of the Yoshioka reference discloses an electron-emitting device that is an electron-emitting device comprising a conductive film 3a containing the electron-emitting portion between one pair of electrodes 1,2.

Regarding claim 27 and 29, Yoshioka discloses an image display apparatus comprising an envelope, an electron-emitting device arrangement in a matrix wiring composed of a plurality of row directional wirings and a plurality of column directional wirings and an image display member for displaying images by irradiation of the electron from the electron-emitting devices where the substrate is as claimed in claim 1 and 9 (col. 32, line 36 – col. 33, line 32).

Claim 28 recites essentially the same limitations of claim 23. Thus claim 28 is rejected as claim 23 (see rejection of claim 23).

Claim 30 recites essentially the same limitations of claim 25. Thus claim 30 is rejected as claim 25 (see rejection of claim 25).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

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Claims 2-3 and 10-11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yoshioka et al (US 5,872,541) in view of Khan et al. (US 5,598,052)

Regarding claims 2 and 10, Yoshioka does not specifically state that the SiO₂ layer contains phosphorus. However, it is well known in the art that for certain bonding processes, it is desirable that the surfaces be planarized. This can be accomplished by depositing phosphorus-doped silicon dioxide and polishing the surface. Alternatively a smooth surface can be obtained by depositing phosphorous-doped silicon dioxide and reflowing it (col. 6, lines 55-61). Accordingly, one of ordinary skill in the art at the time the invention was made would have found it obvious to have the insulating/SiO₂ layer be doped with phosphorus so as to create a smooth flat surface.

Regarding claims 3 and 11, the combination of Yoshioka and Khan discloses the claimed invention except for the limitation of the SiO₂ layer containing phosphorus in a range of 1-10% by wt. It has been held that where the general conditions of a claim are disclosed in the prior art, discovering the optimum or workable ranges involves only routine skill in the art. It would have been obvious to one having ordinary skill in the art at the time the invention was made to provide a workable range, since optimization of workable ranges is considered within the skill of the art.

Claims 6-8 and 14-16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yoshioka et al (US 5,872,541).

Regarding claims 6-8 and 14-16, Yoshioka does not specifically teach the use of another SiO₂ film being deposited over the SiO₂ film that contains the metallic oxide particles. However, it is well known to use an insulating layer such as SiO₂ as a flattening film before depositing the electrodes as evidenced in Fig. 30a-d. In this embodiment, a second SiO₂ layer 5b is formed

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over the first SiO₂ layer 5a and the metallic oxide particles 9 to provide a flat surface for the electrode 2 to be deposited on. Since the SiO₂ layer 11 of Fig. 11(5) contains metallic particles 9 some of which are formed protruding outside the SiO₂ layer 11 one of ordinary skill in the art would at the time the invention was made would have found it obvious to include another SiO₂ layer over layer 11 so as to ensure a flattened surface for the electrodes. It is noted that in Fig. 11(5) the metallic oxide particles 9 are formed in the insulating layer 11 only between the electrodes. However, to simplify the manufacturing steps one of ordinary skill in the art at the time the invention was made would have found it obvious to impregnate the entire layer 11 with metallic oxide particles in which case the second SiO₂ layer would be needed to provide a flattened surface for the electrodes. The second SiO₂ layer 5b of Fig. 30a-d is disclosed to have a thickness that is included in the limitation of 40 nm to 100 nm (col. 29, lines 41-45).

Claims 26 and 31 is rejected under 35 U.S.C. 103(a) as being unpatentable over Yoshioka et al (US 5,872,541) in view of Kaneko et al. (US 5,831,387).

Regarding claims 26 and 31, the electrodes of the Yoshioka reference are composed of platinum as the principal component (col. 23, line 41). The Yoshioka reference is silent with respect to what material the wirings are composed of. However the use of wirings comprising silver as the principal component is well known in the art as evidenced by Kaneko (col. 13, line 61 – col. 14, line 9). Accordingly, one of ordinary skill in the art would have found it obvious at the time the invention was made to have used wirings of composed of silver.

Response to Arguments

Applicant's arguments filed 1/27/04 have been fully considered but they are not persuasive.

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The Applicant argues that Yoshika teaches that the metal oxide particles 9 and insulating film 11 form an electron-emitting element. However, it is not a matter of concern how the particular claimed element functions in the device. As long as the prior art includes all of the structural elements, the claims are anticipated by the prior art.

Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to *Anthony Perry* whose telephone number is **(571) 272-2459**. The examiner can normally be reached between the hours of 9:00AM to 5:30PM Monday thru Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nimesh Patel, can be reached on (571) 272-24597. The fax phone number for this Group is (703) 872-9306.

Communications via Internet e-mail regarding this application, other than those under 35 U.S.C. 132 or which otherwise require a signature, may be used by the applicant and should be addressed to [Anthony.perry@uspto.gov].

All Internet e-mail communications will be made of record in the application file. PTO employees do not engage in Internet communications where there exists a possibility that sensitive information could be identified or exchanged unless the record includes a properly signed express waiver of the confidentiality requirements of 35 U.S.C. 122. This is more clearly set forth in the Interim Internet Usage Policy published in the Official Gazette of the Patent and Trademark on February 25, 1997 at 1195 OG 89.

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Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 308-0956.

Anthony Perry

Patent Examiner Art Unit 2879

March 12, 2004

Vip Patel

Primary Examiner
Art Unit 2879